App No.: Not Yet Assigned Docket No.: M4065.0531/P531-A Inventor: Werner Juengling, et al.
Title: A REVERSE METAL PROCESS FOR CREATING A METAL SILICIDE TRANSISTOR GATE STRUCTURE Sheet 1 of 12

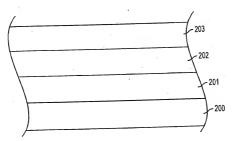


FIG. 1

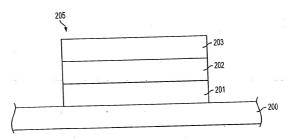


FIG. 2

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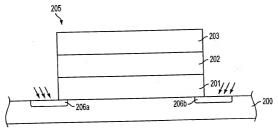


FIG. 3A

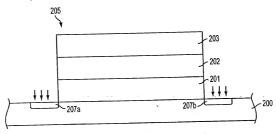


FIG. 3B

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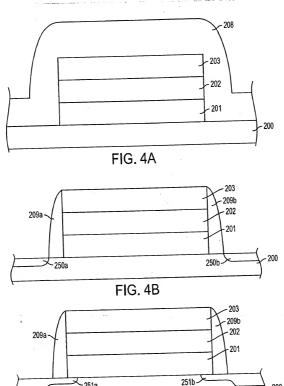
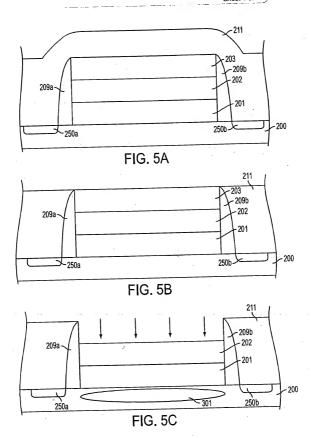


FIG. 4C

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Title: A REVERSE METAL PROCESS FOR CREATING A METAL SILICIDE TRANSISTOR GATE STRUCTURE Sheet 5 of 12 211 209b 202 209a 201 - 200 250b 250a FIG. 6A 271 211 209b 202 209a 201 200 301 250b FIG. 6B 209b 202 209a -201 200 $\geq_{\underline{301}}$

FIG. 6C

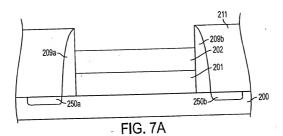
303

250b

250a

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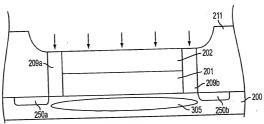
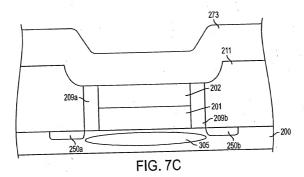


FIG. 7B

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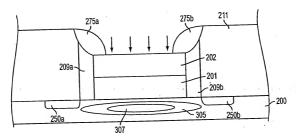


FIG. 7D

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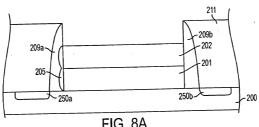


FIG. 8A

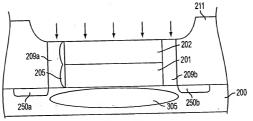


FIG. 8B

App No.: Not Yet Assigned procedure. M4065.0531/P531-A Inventor; Werner Juengling, et al. at Itle: A REVERSE METAL PROCESS FOR CREATING A METAL SILICIDE TRANSISTOR GATE STRUCTURE Sheet 9 of 12

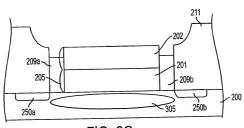
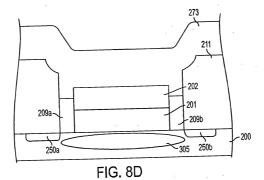
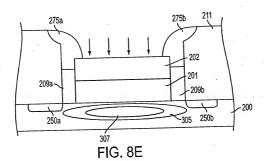


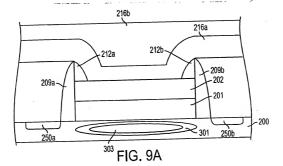
FIG. 8C

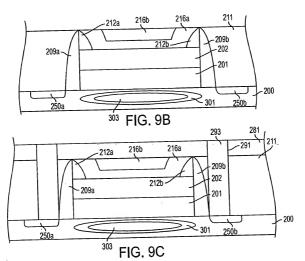
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